



Substitute for form 1449A

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

1

of

2

Complete if Known

Application Number	09/894,310
Filing Date	June 27, 2001
First Named Inventor	Richard L. Coulson
Art Unit	2188
Examiner Name	Midys Inoa
Attorney Docket Number	42390P11448

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
MI	1	US-6,370,614 B1	04-09-2002	Teoman et al.	
MI	2	US-6,463,509 B1	10-08-2002	Teoman et al.	
MI	3	US-2003/0005219 A1	01-02-2003	Royer Jr. et al.	
MI	4	US-2003/0188123 A1	10-02-2003	Royer Jr. et al.	
MI	5	US-2003/0046493 A1	03-06-2003	Coulson	
MI	6	US-2003/0061436 A1	03-27-2003	Royer Jr. et al.	
MI	7	US-2003/0074524 A1	04-17-2003	Coulson	
MI	8	US-2003/0120868 A1	06-26-2003	Royer Jr. et al.	
MI	9	US-2003/0188251 A1	10-02-2003	Brown et al.	
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FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)			
MI	10	EP 0 702 305 A1	03-20-1996	Kitagawa	

Examiner
Signature

Midys Inoa

Date
Considered

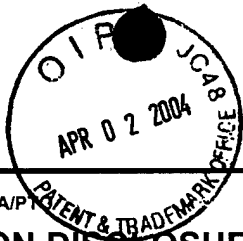
4/16/04

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. *Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

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Substitute for form 1449A/P INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known	
				Application Number	09/894,310
Sheet 2 of 2				Filing Date	June 27, 2001
				First Named Inventor	Richard L. Coulson
				Art Unit	2188
				Examiner Name	Midys Inoa
				Attorney Docket Number	42390P11448

NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
MI	11	US Patent Application, pending, 09/602,011 filed 6/23/03 to Richard Coulson, "Non-Volatile Cache Integrated with Storage Devices"
MI	12	US Patent Application, pending, 09/669,770 filed 9/26/00, to Richard Coulson, "Non-Volatile Mass Storage Cache Coherency Administrator"
MI	13	US Patent Application, pending, 09/602,008 filed 6/23/00, to Richard Coulson, "In-Line Cache"
MI	14	US Patent Application, pending, 09/602,009 filed 6/23/00, to Richard Coulson, "Non-Volatile Cache Expansion Board"
MI	15	Jack Robertson, Silison Strategies, "Hyundai enters Japanese-dominated FeRAM Market", September 3, 1998
MI	16	EEdesign, "Ramtron boosts serial FRAM density to 64 Kbits", October 6, 1999
MI	17	Ramtron International Corp., data sheet, "The FRAM Technology", January 1994
MI	18	Ramtron International Corp., data sheet "Preliminary - FM24C64 64Kb FRAM Serial Memory", December 7, 1999
MI	19	Ramtron International Corp., data sheet "Preliminary - FM1608 64Kb Byte-wide FRAM Memory", December 7, 1999
MI	20	W. Merz et al., Bell Laboratories Record, "Ferroelectric Storage Devices", September 1955
MI	21	O. Auciello et al., Physics Today, "The Physics of Ferroelectric Memories", July 1998
MI	22	C. F. Pulvarti, The Catholic University of America, IRE Transaction - Component Parts, "Ferroelectrics and their Memory Applications", 1956
MI	23	J.L. Hennessy et al., "Computer Architecture: A Quantitative Approach" 3rd Edition, 2003, pages 454-457 and pages 689-692

Examiner Signature	<i>Midys Inoa</i>	Date Considered	4/16/04
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